## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

I≰Re Application of: FLETCHER, Lawrence P. et al.

Group Art Unit: 2819

Serial No.

10/635,263

Examiner:

Filed:

08/06/2003

Atty. Dkt. No: MS001-US

FOR: DITHERING MODULE WITH DIPLEXER

To:

Mail Stop IDS

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24222

Commissioner for Patents

PO Box 1450

Alexandria, VA 22313-1450

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11/12/2002

Debra A. Stengel

Commissioner:

## TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT

Transmitted herewith is the Applicant's Information Disclosure Statement, 1 sheet form 1449, and 8 references and other information. Pursuant to 37 CFR 1.98 (a)(2)(i) applicant has not transmitted herewith copies of cited U.S. Patents and U.S. patent application publications as the above application was filed after June 30, 2003. Please enter in the above application and communicate in all related matters with the undersigned.

All necessary fees are intended to be included, however the Office is hereby authorized to charge any deficiency or credit any overpayment in the fees to deposit account #500323.

Respectfully submitted,

Vernon C. Maine, Reg. No. 37,389

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To: Mail Stop IDS Commissioner PO Box 1450 Alexandria, V	for Patents	Fm:	24222	
CERTIFICATE OF MAIL the U.S. Postal Service wit Patents, PO Box 1450, Ale	h sufficient postage as FIR	that this correspo ST CLASS MA	IL addressed to: Mail	sited on the below date with Stop IDS, Commissioner of Storngol
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Date:	[ ] Vernon C	. Maine, Reg. N	o. 37,389 or [ ] Scott	J. Asmus, Reg. No. 42,269

Commissioner:

## INFORMATION DISCLOSURE STATEMENT

Applicants submit this statement and the attached 1 sheet(s) of form PTO-1449 and 8 references and other information in accordance to the duty of disclosure under 37 C.F.R. §§1.56,1.97, and 1.98, and requests consideration hereof. Pursuant to 37 CFR 1.98 (a)(2)(i) applicant has not transmitted herewith copies of cited U.S. Patents and U.S. patent application publications as the above application was filed after June 30, 2003.

Compliance with 37 C.F.R. §1.97: This Information Disclosure Statement is filed within three (3) months of the filing date of a National Application or before the mailing date of a first office action on the merits. No fee or certification is required.

<u>Information Cited:</u> The Applicants hereby make of record in the above-identified application, the information listed on the attached form PTO-1449 (modified). The order of presentation of the references should not be construed as an indication of the importance of the reference. As all the references listed on attached Form PTO-1449 are in English, no commentary is required.

<u>Remarks:</u> A copy of each reference, together with a listing on Form PTO-1449(modified), is submitted herewith. Applicants respectfully request that:

- 1. The Examiner consider completely the cited information, along with any other information, in reaching a determination concerning the patentability of the present claims;
- 2. The enclosed form PTO-1449 (modified) be signed by the Examiner to evidence that the cited information has been fully considered by the Patent and Trademark Office during the examination of this application; and
- 3. The citations for the information be printed on any patent which issues from this application.

By submitting this Information Disclosure Statement, the Applicants make no representation that a search has been performed, of the extent of any search performed, or that more relevant information does not exist.

By submitting this Information Disclosure Statement, the Applicants make no representation that the information cited in the Statement is, or is considered to be, material to patentability as defined in 37 C.F.R. §1.56(b).

By submitting this Information Disclosure Statement, the Applicants make no representation that the information cited in the Statement is, or is considered to be, in fact, prior art as defined in 37 C.F.R. §102.

Notwithstanding any statement by the Applicant, the Examiner is urged to form his own conclusion regarding the relevance of the cited information.

An early and favorable action is hereby requested.

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Respectfully submitted,

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Attorneys/Agents for Applicant

Form PTO-1449				Complete if Known		
	ATE	₩7 e πα!	DEMAR	Application Number	10/635,263	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Filing Date	08/06/2003	
				First Named Inventor	FLETCHER, Lawrence P.	
					2819	
i i				Examiner Name		
Sheet	1	of	1	Attorney Docket Number	MS00-US	

U.S. PATENT DOCUMENTS						
Examiner Initials	Cite No.	U.S. Patent Docu Number Code	ument Kind	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		4,467,472		Wendt	08-21-1984	
		5,959,592		Petruzzelli	09-28-1999	
_		6,515,542	B1	Wang et al	02-04-2003	
**						

			FOREI	GN PATENT DOCU	UMENTS		
Examiner Initials	Cite No.	Foreign Patent Docur Office   Number Code	ment Kind	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	Т

	OTHER PRIOR REFERENCES - NON PATENT LITERATURE DOCUMENTS						
Examiner Initials	Cite No. (Including Name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (bo magazine, journal, serial, symposium, catalog, etc), date, page(s), volume-issue number(s), publisher, city, and/or where published.		Т				
		MICRONETICS, MDM-70 Dithering Modules, Hudson, NH USA					
		MICRONETICS, Mini-Noise Diodes 10 KHz to 3 GHz, Hudson, NH USA					
		MICRONETICS, Noise Diodes 10Hz to 110GHz, Hudson, NH USA					
		ANALOG DEVICES, AN-410 Application Note, pages 1-8, Norwood, MA USA					
		LINEAR TECHNOLOGY, LT1818/LT1819 400MHz, 2500V/µs, 9mA Single/Dual					
	<u></u>	Operational Amplifiers, pages 1-16, Milpitas, CA USA					
		LINEAR TECHNOLOGY, LT1129/LT1129-3.3/LT1129-5 Micropower Low Dropout					
		Regulators with Shutdown, pages 1-16, Milpitas, CA USA					
		LINEAR TECHNOLOGY, LTC1566-1 Low Noise 2.3MHz Continuous Time Lowpass					
		Filter, pages 1-8, Milpitas, CA USA					
		LINEAR TECHNOLOGY, LT1797 10MHz, Rail-to-Rail Input and Output Op Amp in SOT-					
		23, pages 1-12, Milpitas, CA USA					

* In accordance with 37 CFR 1.98(d) a copy	of this reference is not	provided as it was previously submitted to, or cited by,
the Office in prior Application No.	filed	, which is being relied on for an earlier effective
filing date under 35 U.S.C. § 120.		

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Examiner		Date	
Signature		Considered	and the second second second second

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.